NOTES: (UNLESS OTHERWISE SPECIFIED)

1. ALL RESISTANCES ARE IN OHMS, 1/4W, 5%.
2. ALL CAPACITOR VALUES ARE IN MICROFARADS.

REV.	DESCRIPTION OF CHANGE	ECO	DATE	BY	APPR
B	REL. TO PRE-PROD.	1030	6/14/93	F.B.	SRK
C	STANDARDIZE & PROD. REL.	1486	7/8/94	F.B.	SRK
D	ADD RC FILTERS, IMPED. SEL. JMPS	1806	7/15/94	F.B.	SRK
D	TRANS. FROM OEM		9/5/96		Y.M.

TIMING CIRCUIT (SHEET 2)

EVEN
ODD
PHASE
*ZERO
*RST
QSW_TRIG

2923434D.SCH

CONTROL CIRCUIT (SHEET 3)

EVEN
ODD
PHASE
OVER
*ZERO
PIEZO-200
*DISABLE
PV+
PV-

2933434D.SCH

REGULATION CIRCUIT (SHEET 4)

QSW_TRIG
*ZERO
*RST
EVEN
OVER
PIEZO-200
*DISABLE
PV+
PV-
*DIODE_ON

2943434D.SCH

+12: C32 C18 C20 C26 C9 C14 C41 C43

LOGIC POWER / GND		
IC NO.	+5	GND
U3	16	8
U5	14	7
U6	16	8
U10	14	7

-12: C31 C28 C17 C8 C11 C25 C40 C42

VCC: C22 C15 C6 C13

+8: C34 100

-8: C35 100

-12: C36 100

+12: C37 100

J5: +8VDC, -8 VDC, -12VDC, +12VDC, +5VDC, *DIODE_ON, +450VDC

VCC: C38 100

TP1 GND

+450: C39 .0033 1KV

SPARES

U10B 74ALS00
U10C 74ALS00
U11A MC33084
U4A LM339
U7B MC33084
U4D LM339

Title	SCHEMATIC, CONTROL, SEEDER		
Size: Orcad C	Number: 0129-2434		Revision: D
Date: 13-Jul-1994	Sheet 1 of 4		
File: D:\PROTEL\SEEDCTRL\29-2434D.PRJ	Drawn By: SRK		